

<b>Notice of References Cited</b>	Application/Control No. 10/617,451	Applicant(s)/Patent Under Reexamination NGO ET AL.	
	Examiner Walter L. Lindsay, Jr.	Art Unit 2812	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,518,070	02-2003	Alluri et al.	438/3
	B	US-5,693,566	12-1997	Cheung, Robin W.	438/624
	C	US-6,559,007	05-2003	Weimer, Ronald A.	438/257
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., "Silicon Processing for the VLSI Era", 1986, Lattice Press, Vol. 1, pg. 398
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.